



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Koichiro Tanaka

Art Unit : 2822

Serial No. : 09/842,797

Examiner : Maria F. Guerrero

Filed : April 27, 2001

Title : METHOD OF FABRICATING SEMICONDUCTOR DEVICE

MAIL STOP RCE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

A copy of the reference listed on the attached form PTO-1449 is enclosed.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: 10/20/04



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Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 12732-034001	Application No. 09/842,797
	Applicant Koichiro Tanaka		
	Filing Date April 27, 2001	Group Art Unit 2822	

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	6,534,744	03/18/2003	Yamazaki et al.			07/13/2000
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AQ	
	AR	
	AS	
	AT	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	